

Highlights are:

- Hands on experience with FPGA design verification and simulation languages and tools (including Vera, Specman, System Verilog)
- Experience with lab bring up, debugging and design validation of FPGA
- Proficiency with hardware test and measurement equipment including oscilloscope and logic analyzer
- RTL design (Verilog/VHDL) expertise
- Board design expertise is a plus
- Some experience with PCI express

Note: Requires 3-5 years experience with System Verilog

External Job Description:

The job involves working as part of a team verifying high end network FPGAs, the candidate will be involved with all aspects of FPGA verification including but not limited to:

- infrastructure development
- authoring of test plans
- test plan execution
- coverage and code reviews

The candidate is also expected to contribute throughout all stages of the FPGA development process:

- Participate in architecture analysis & definition
- architectural and performance modeling
- micro-architecture reviews
- follow standard FPGA design verification, test processes, and procedures
- Collaborate with architects, FPGA design engineers, tool vendors, software engineers, in verifying new FPGA
- Test & debug FPGA; Work closely with design engineer in debugging and resolving issues
- Collaborate with cross functional teams to ensure the FPGA meets all software, diagnostic and system level requirements
- Work with cross functional team during FPGA system power on, lab bring-up. Perform validation / debug, root cause & resolve problems
- Debug and replicate failures in verification environment on released FPGA designs for critical manufacturing and/or field issues

Manager Specific Requirements:

- Proven track record of FPGA verification and releasing high quality products to manufacturing
- Very high technical competence, individual technical accomplishments with repeated, proven track record for product delivery to market
- Expertise at complex FPGA design verification and gate level simulation
- Hands on experience with FPGA design verification and simulation languages and tools (including Vera, Specman, System Verilog, C/C++, Denali, Debussy)
- RTL design (Verilog/VHDL) expertise
- Experience with designing in a Unix environment; Scripting skills (such as shell, Perl, TCL)
- Experience with lab bring up, debugging and design validation of FPGA
- Proficiency with hardware test and measurement equipment including oscilloscope and logic analyzer
- Strong leadership and demonstrated ability to achieve stretch goals in an innovative and fast paced environment
- Strong verbal and written communications skills
- Team-player, can-do attitude, will work well in a group environment while being able to contribute well on an individual basis
- Able to plan and execute tasks with minimal supervision
- Working knowledge of networking protocols, products and technologies
- Board design expertise is a plus
- 7 years experience in FPGA design verification with BS (MS is a plus) in electrical/computer engineering or equivalent training